

GPIO ASSIGNMENT

PIN	Define	CFG	Function
PA0	UART0_TX	5	DEBUG
PA1	UART0_RX	5	
PA2	BT_UART2_CT	8	BT
PA3	BT_UART2_RT	8	
PA4	CAN0-TX	4	CAN
PA5	CAN0-RX	4	
PA8	RTP-XP-SCL	2/4	RTP/CTP
PA9	RTP-YP-SDA	2/4	
PA10	RTP-XN-RST	2/1	
PA11	RTP-YN-INT	2/1	

PIN	Define	CFG	Function
PB0	SPI0-WP	2	NOR/ NAND
PB1	SPI0-MISO	2	
PB2	SPI0-CS	2	
PB3	SPI0-HOLD	2	
PB4	SPI0-CLK	2	
PB5	SPI0-MOSI	2	WIFI
PB6	SDC0-CMD	2	
PB7	SDC0-CLK	2	
PB8	SDC0-D3	2	
PB9	SDC0-D0	2	
PB10	SDC0-D1	2	
PB11	SDC0-D2	2	

PIN	Define	Define	CFG	Function
PD0	LCD0-D0	I2C0-SCL	4	I2C0
PD1	LCD0-D1	I2C0-SDA	4	
PD2	LCD0-D2	UART1-TX	5	RS485
PD3	LCD0-D3	UART1-RX	5	
PD4	LCD0-D4	BT_UART2_TX	5	BT
PD5	LCD0-D5	BT_UART2_RX	5	
PD6	LCD0-D6	BT_PWR_ON	1	WIFI
PD7	LCD0-D7	WIFI_PWR_ON	1	
PD8	LCD0-D8	USB-ID	1	USB
PD9	LCD0-D9	LCD_SLEEP	1	
PD10	LCD0-D10	PA_SHDN	1	SPEAKER
PD11	LCD0-D11	I2S-DIN	4	I2S/PCM
PD12	LCD0-D12	I2S-DOUT	4	
PD13	LCD0-D13	I2S-LRCK	4	
PD14	LCD0-D14	I2S-BCLK	4	
PD15	LCD0-D15	KEY_WAKEUP	1	
PD16	LCD0-D16	DMIC_CLK	4	DMIC
PD17	LCD0-D17	DMIC_DATA	4	
PD18	LCD0-D18	LVDS_D0N	3/4	LVDS /MIPI
PD19	LCD0-D19	LVDS_D0P	3/4	
PD20	LCD0-D20	LVDS_D1N	3/4	
PD21	LCD0-D21	LVDS_D1P	3/4	
PD22	LCD0-D22	LVDS_D2N	3/4	
PD23	LCD0-D23	LVDS_D2P	3/4	
PD24	LCD0-DCLK	LVDS_CKN	3/4	
PD25	LCD0-HS	LVDS_DKP	3/4	
PD26	LCD0-VS	LVDS_D3N	3/4	
PD27	LCD0-DE	LVDS_D3P	3/4	

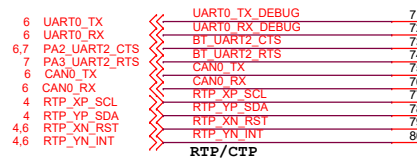
PIN	Define	CFG	Function
PE0	MAC0-RXD1	2	EMAC 100M
PE1	MAC0-RXD0	2	
PE2	MAC0-CRS-DV	2	
PE3	MAC0-REFCLK	2	
PE4	MAC0-TXD1	2	
PE5	MAC0-TXD0	2	
PE6	ETH0_RST	1	
PE7	MAC0-TXEN	2	
PE8	MAC0-MDC	2	
PE9	MAC0-MDIO	2	
PE10	EPHY0-25M	2	
PE11	BUZZER	3	PWM1_A
PE12	DSPK1	5	SPEAKER
PE13	LCD_PWM	3	PWM2_A

PIN	Define	CFG	Function
PC0	SDC1-D1	2	CARD
PC1	SDC1-D0	2	
PC2	SDC1-CLK	2	
PC3	SDC1-CMD	2	
PC4	SDC1-D3	2	
PC5	SDC1-D2	2	
PC6	SDC1-DET	2	

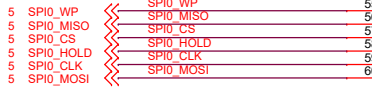
PIN	Define	CFG	Function
PU0	USB-DM	2	USB
PU1	USB-DP	2	

Ver.	Change list	Date
V1.0	Initial Version	2023.7.5
V1.1	Optimize DC5V power and consumption, Keyadc change resistor voltage divider	2023.9.5
V1.2	Fix the WIFI_D1 and WIFI_D0 error	2023.9.16

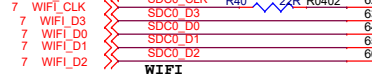




NAND/NOR

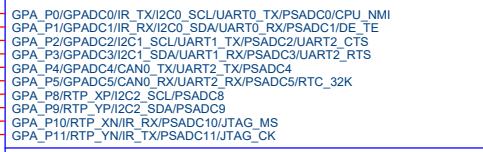


SDIO CMD

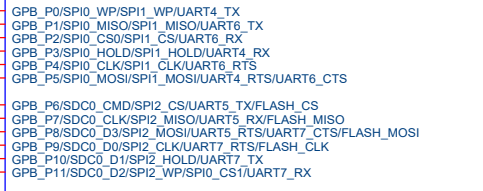


WIFI

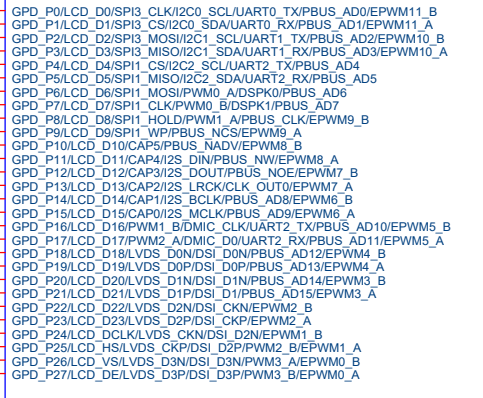
GPADC/RTP/I2C0~2/UART0~2/CIR/CAN0



SPI0~2/SDC0/UART4~7

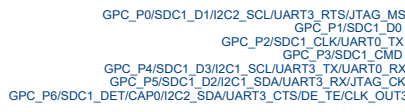


LCD/LVDS/MIPI-DSI/SPI1&3/I2S/UART0~2



D133CBS

SDC1/I2C1~2/UART0&3



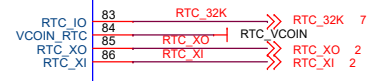
EMAC/DVP/I2S/UART3~7/DMIC/DSPK



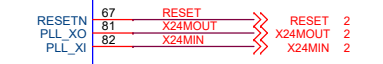
USB/UART0



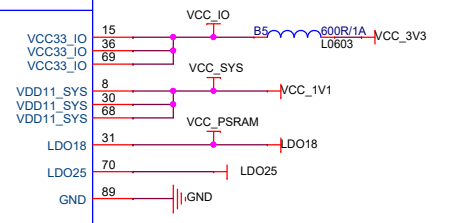
RTC



SYS

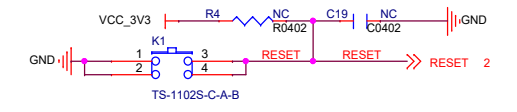
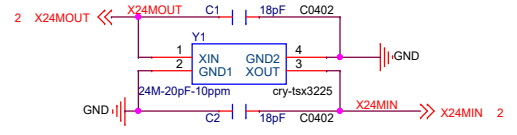


POWER

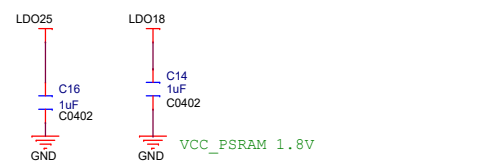
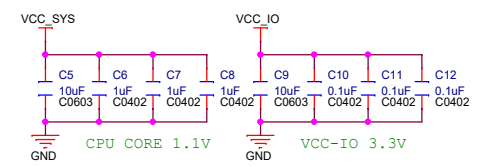


CPU底部GND焊盘加强散热

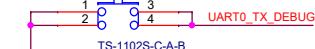
PLL



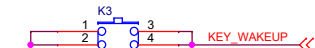
CPU decouple cap



UBOOT-KEY

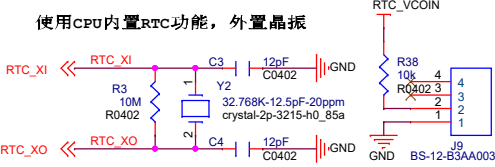


WAKEUP-KEY



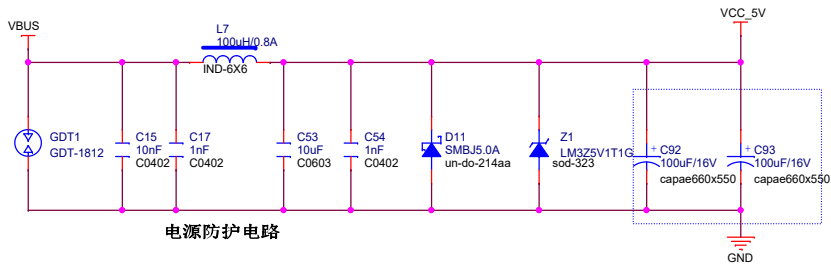
按键唤醒，低电平有效，用于调试休眠功能

RTC

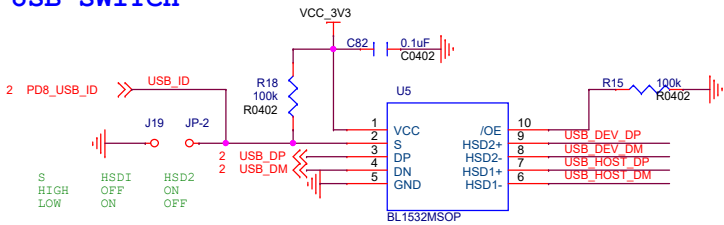


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Size	Page Name	CPU	
A3			Rev V1.0
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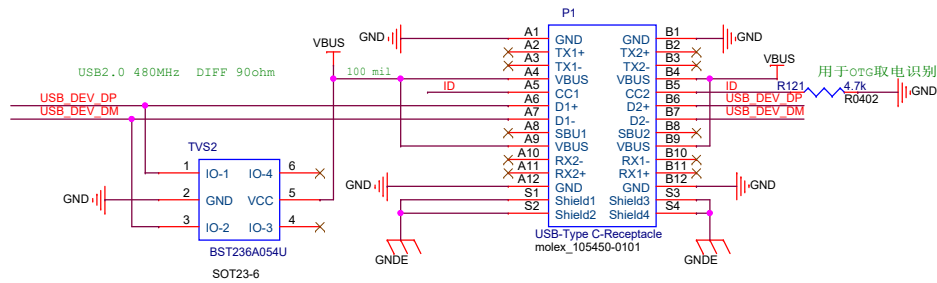
DC5V POWER



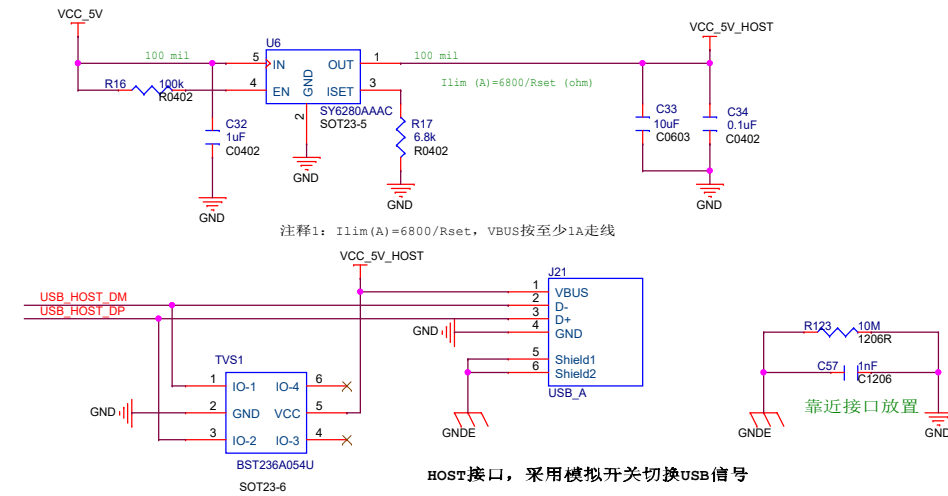
USB SWITCH



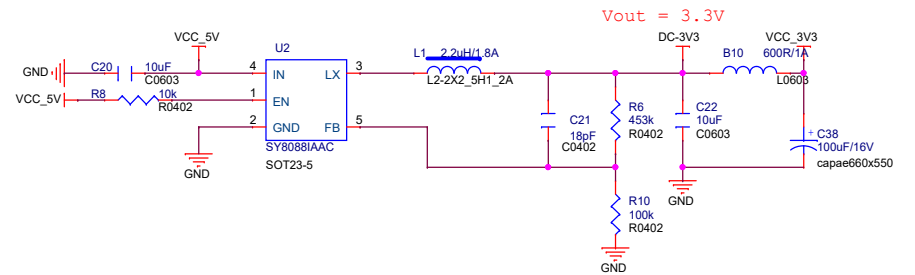
USB DEVICE



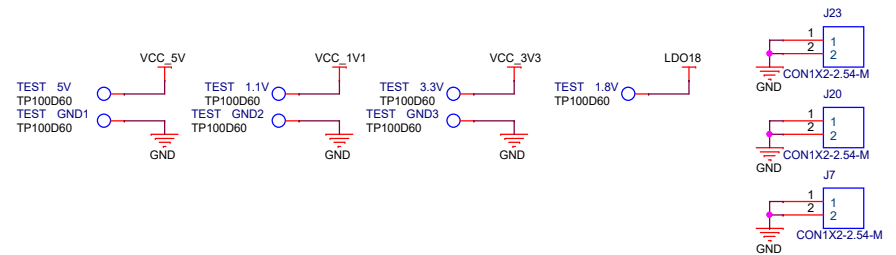
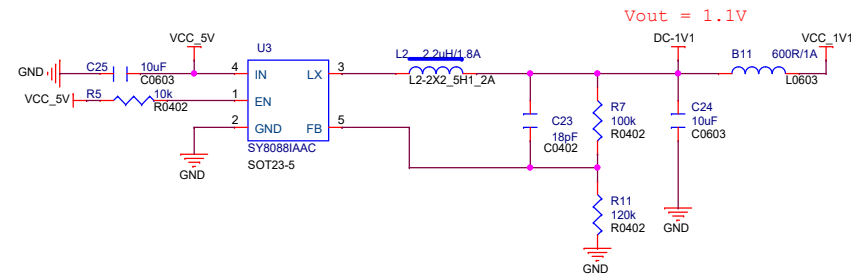
USB HOST



3.3V for VCC-IO



1.1V for VCC-SYS

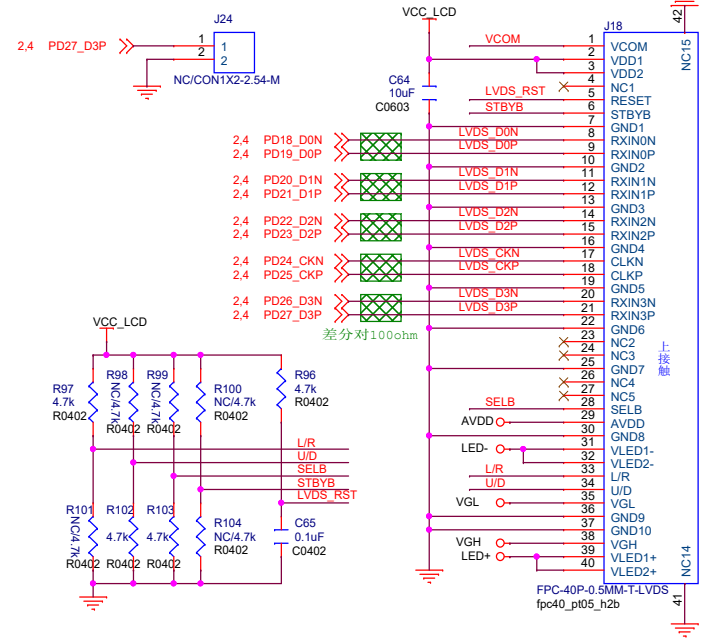


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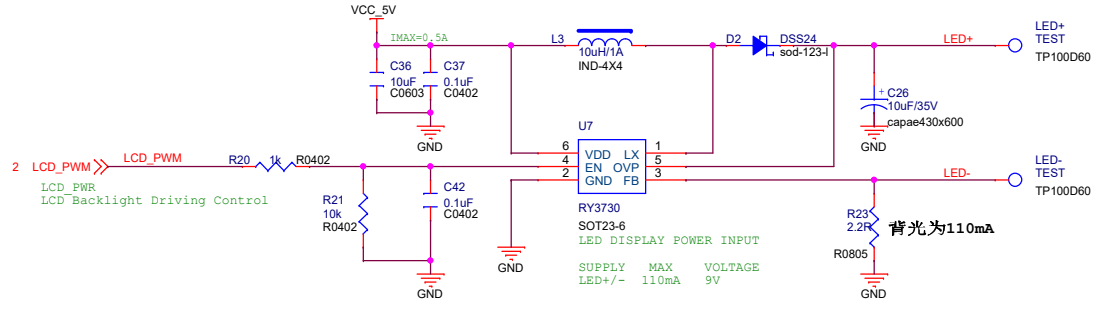
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			Design Name D133CBS-DEMO	
Size A3	Page Name USB/POWER		Rev V1.1	
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LVDS

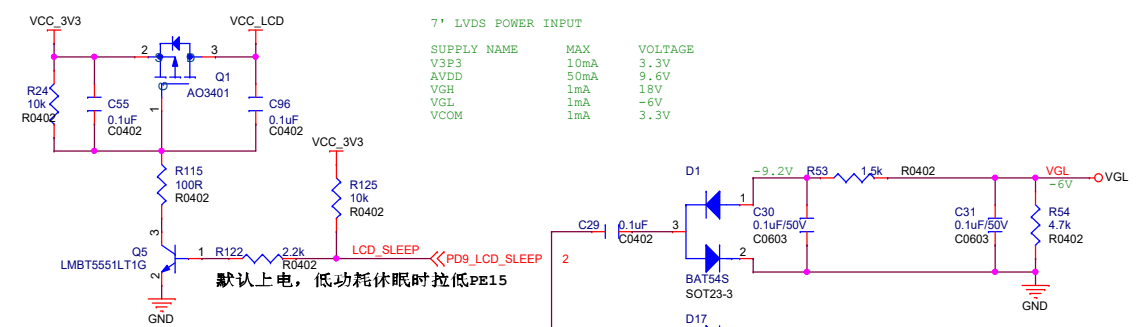
7" 40Pin 上接触



LCD BACKLIGHT

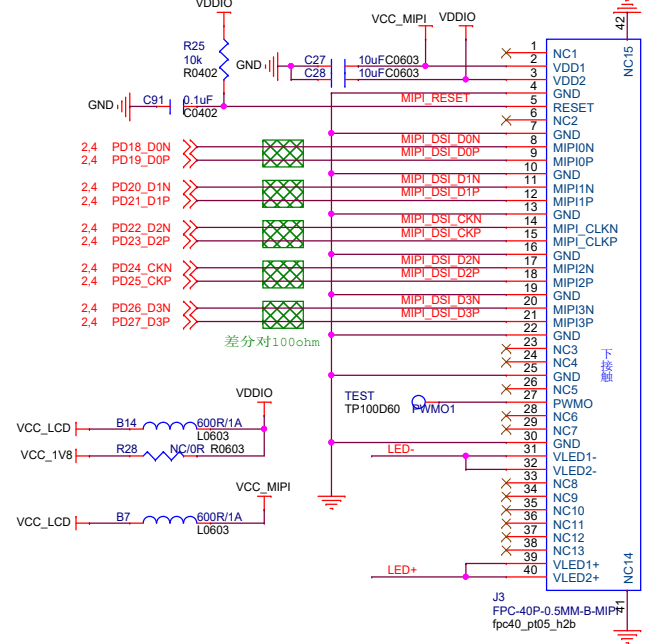


LCD POWER

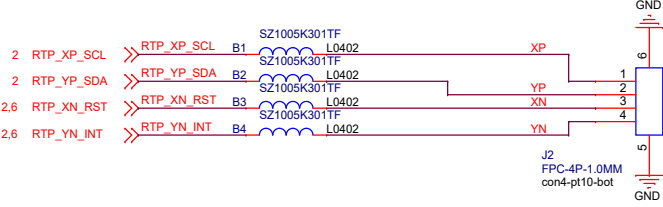


MIPI-DSI

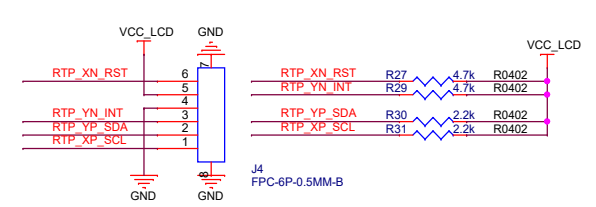
10.1" 40Pin 下接触



RTP



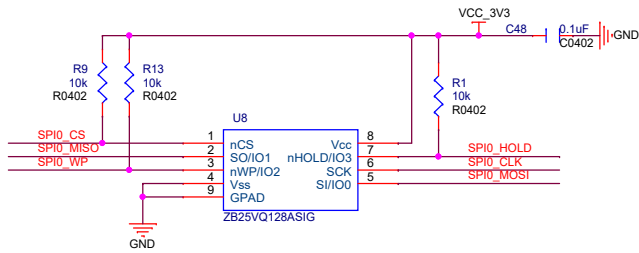
CTP



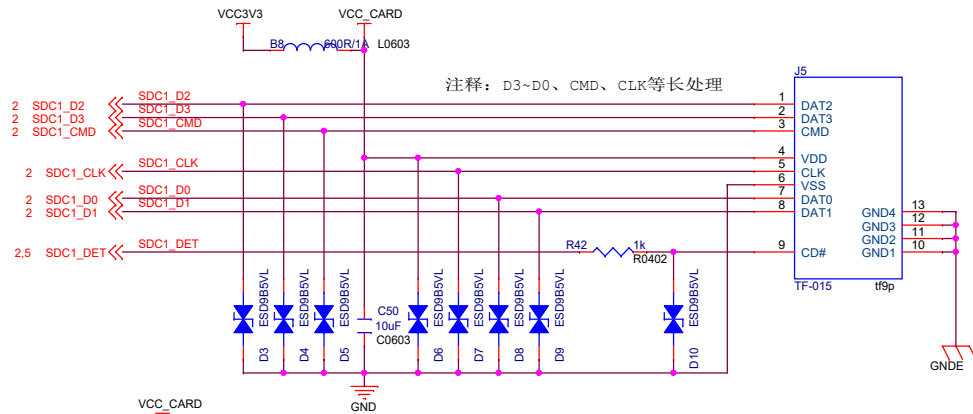
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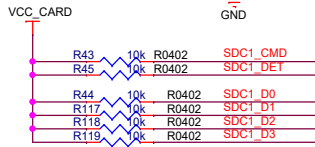
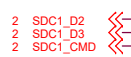
QSPI NOR/NAND



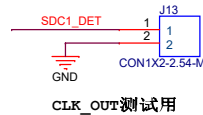
TF-CARD



注释: D3~D0、CMD、CLK等长处理




D0~D3增加上拉电阻



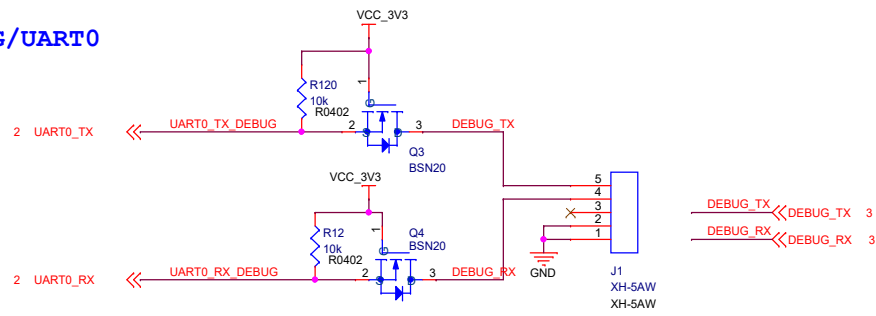
CLK_OUT测试用



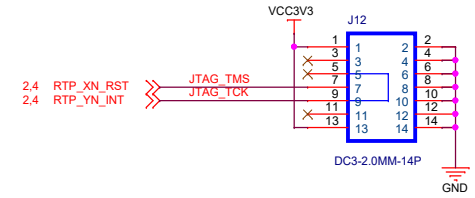
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Size A3	Page Name NAND/NOR/CARD	Rev V1.0	
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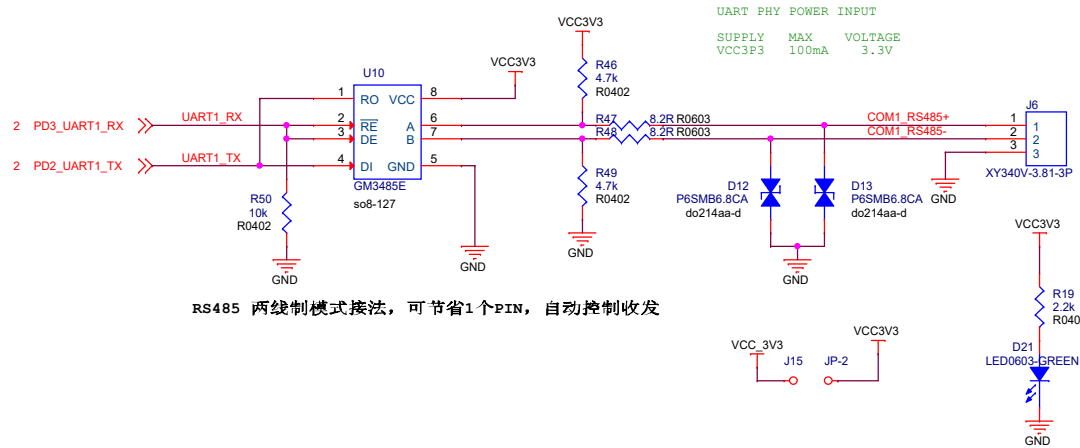
DEBUG/UART0



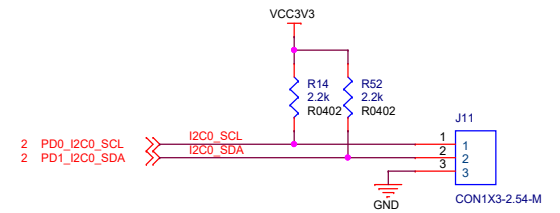
JTAG (Option)



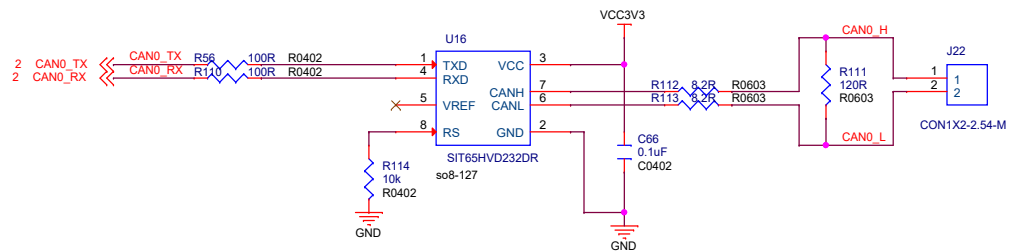
RS485/UART1



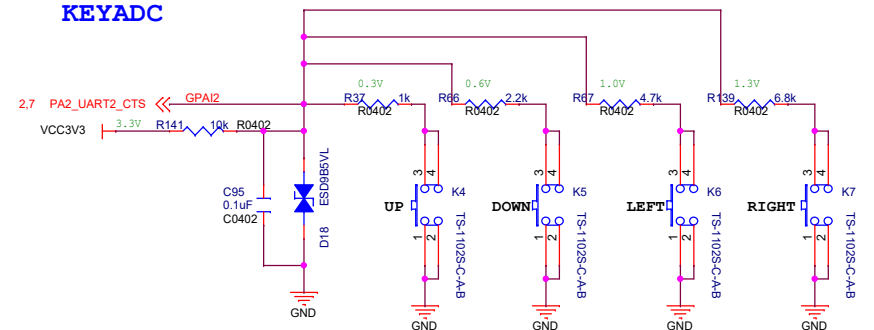
I2C0




CAN



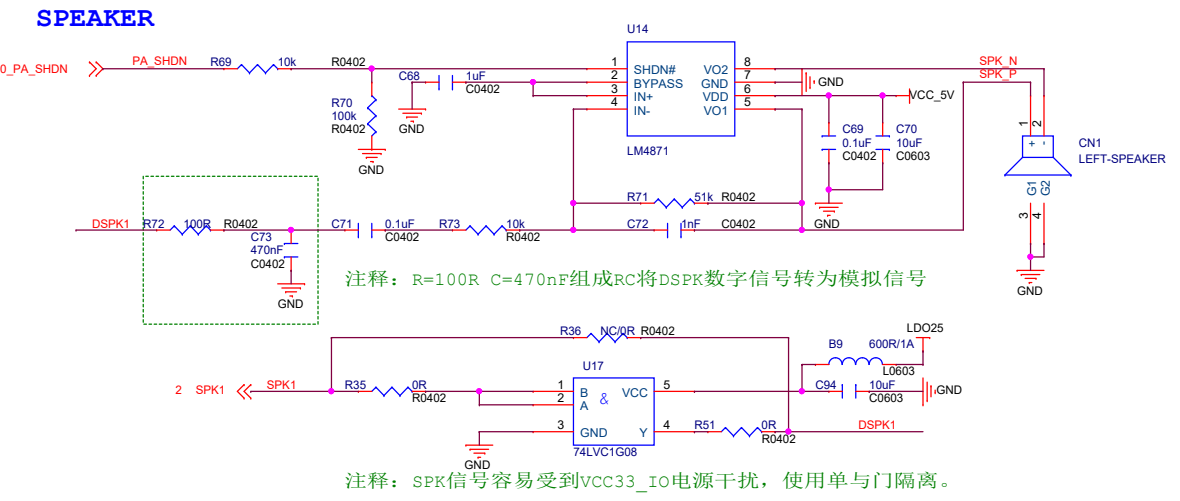
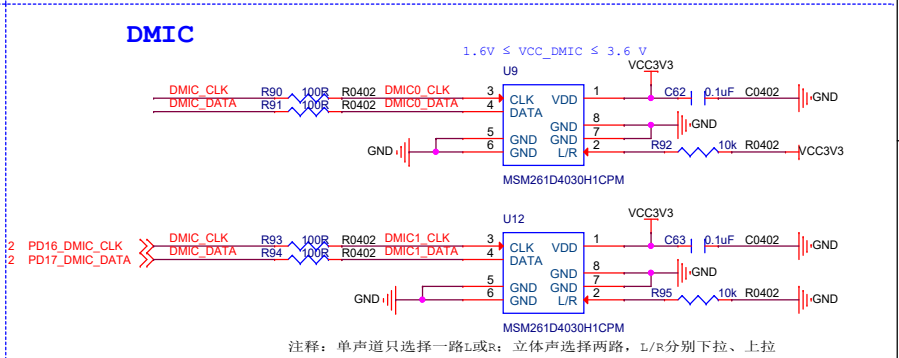
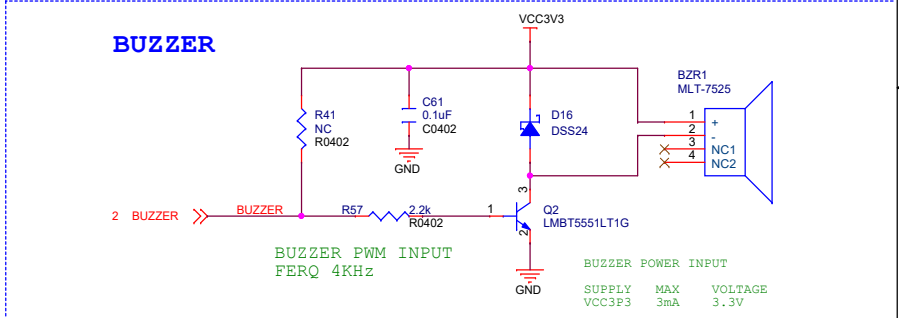
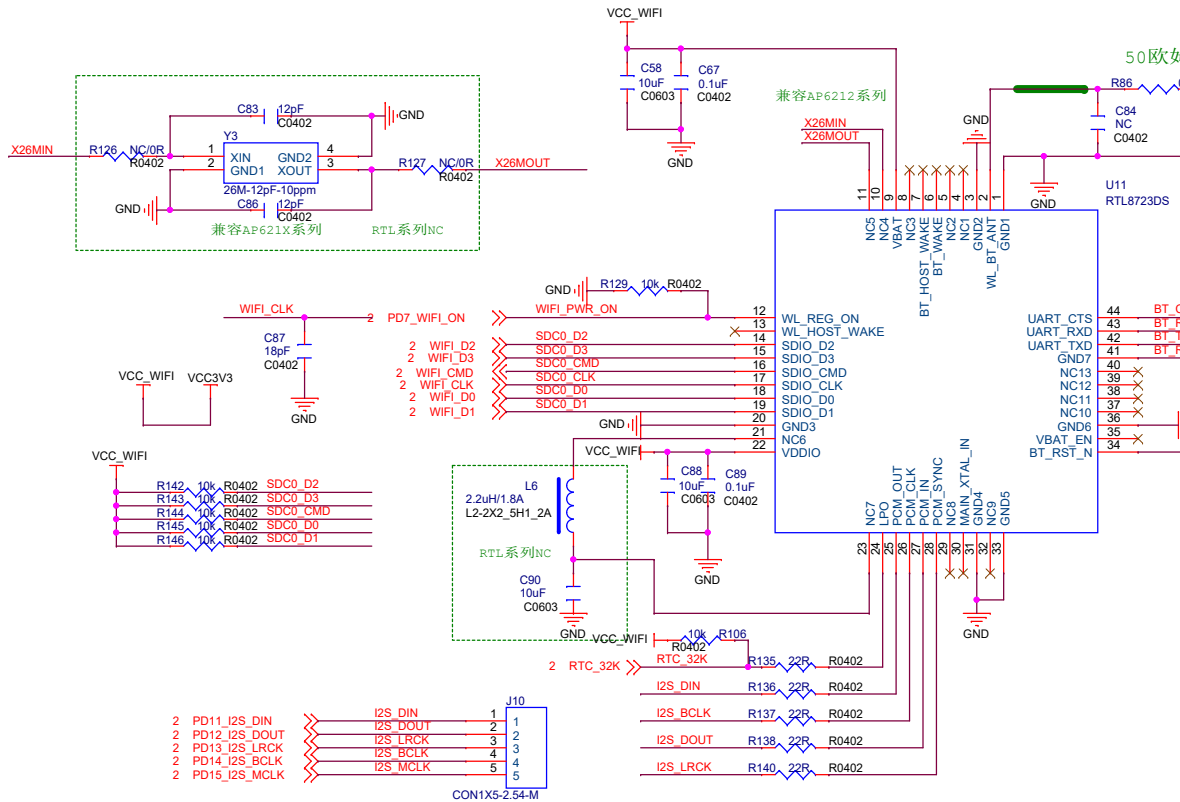
KEYADC



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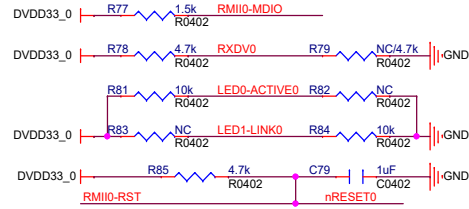
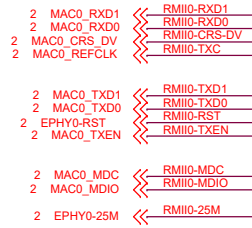
WIFI+BT+I2S



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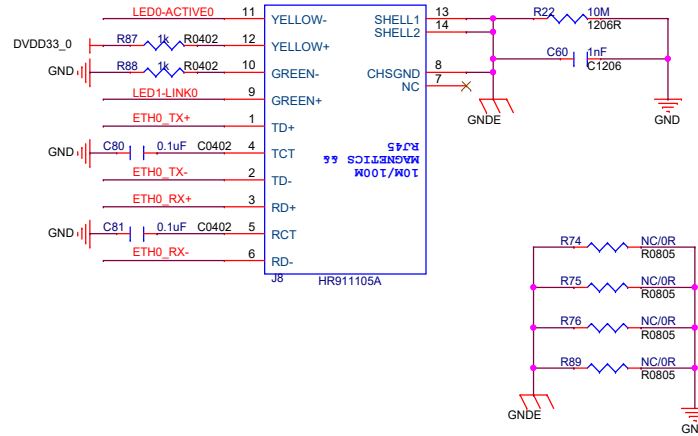
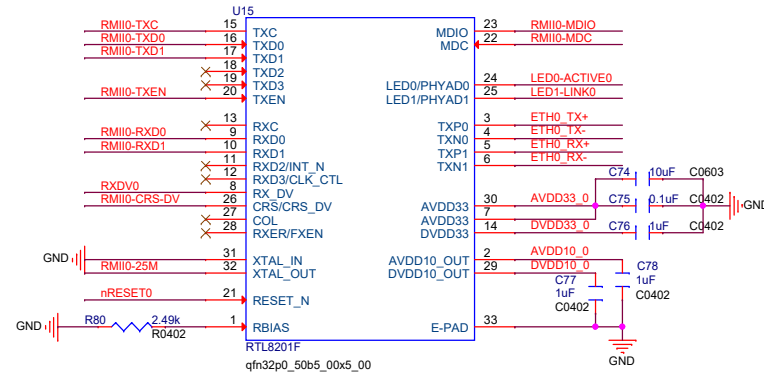
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Size: A3	Page Name: WIFI+BT+I2S/SPK/DMIC/BUZZER	Rev: V1.2
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RMII0-100M



	Pull high	Pull down
RXDV	RMII mode	MII mode
RXD3(内部PD)	TXC input	TXC output
RXD1(内部PD)	WOL mode	LED mode

PHY-ADDR	LED1-LINK	LED0-ACTIVE
addr = 5' d0	Pull down	Pull down
addr = 5' d1	Pull down	Pull high
addr = 5' d2	Pull high	Pull down
addr = 5' d3	Pull high	Pull high



<Variant Name>